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RF Modular Design IAP MIT Lincoln Laboratory 12 JHW 5/12/2011 Parallel circuit -common voltage $R T = R 1R 2 / (R 1+R 2)$ Circuit Theory -Kirchhoff's Laws • Kirchhoff's voltage law: sum of voltages around any closed loop is zero • Kirchhoff's current law: sum of currents into any node is zero • In general N equations for N loops or nodes R 1 R 2 R 3 1R 2 R T

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